

## The CMX-TINY+\* RTOS Kernel

- Supports Intel 8051, 80151, 80251, 80196 Microcontrollers
- No Royalties on Embedded Code
- All Source Code Supplied
- Fast Context Switch Times
- Low Interrupt Latency Times
- Small ROM Requirements
- Small RAM Requirements
- Full Pre-Emptive RTOS

The CMX-TINY+\* Real-Time Multi-Tasking Operating System is a "Tiny Plus" kernel for the Intel microcontrollers that have a fair amount of RAM, embedded on the processor's silicon (approximately 512 bytes and up). This allows the user to develop their application code and have it run under a RTOS, using just the RAM that the processor provides. The CMX-TINY+ does NOT need any external RAM, regardless of whether the processor can support the use of external RAM or not. The code size of the CMX-TINY+ is also very small, thus allowing the processor's on board ROM, to support both the user application code and the CMX-TINY+ code in most cases. This kernel, based on a scaled down CMX CMX-RTX\*, retains the most frequently used functions and associated functionality. True preemptive scheduling is provided, with cooperative scheduling also available, if needed.

### MICROCONTROLLERS SUPPORTED:

8xC196Kx, 8xC196Mx, 8xC196Nx, 8x3x, 8xC5x, 80C51FA/RA/GB/BH/SLAH/SLAL, 87C51FA/RA/RB/RC/GB/SLAH/SLAL, 83L51Fx, 87L51Fx, 83C51Rx/Gx/KB/Fx/SLAH/SLAL, 8xC152Jx, 8xC151Sx, 81C51SLAH/SLAL, 8xC251Sx, 82930A

### DEVELOPMENT PLATFORMS:

PC, SUN PC/Windows, HP Workstation, RS6000

### AVAILABILITY:

Now

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For international contacts, see Appendix B.

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